

**In the Specification**

Please amend paragraphs [0025], and [0031], as noted below.

**[0025]**When SELECT1 is high (5 volts) and SELECT2 is low (0 volts), M20 is on and M21 is off such that the demultiplexer input is coupled through M20 through the first demultiplexer output terminal to the 5 volt driver stage and the second demultiplexer output terminal is off (i.e., M30 is off). When SELECT1 is low (1.7 volts) and SELECT2 is high (2.5 volts), M20 is off and M21 is on such that the demultiplexer input is instead coupled to the second demultiplexer output terminal through the PMOS current mirror (M22, M23), cascode transistor M26, NMOS current mirror (M27, M28), and the current flow terminals of switch transistor M30. M30 is on by virtue of SELECT2 being high.

**[0031]**The high voltage driver (e.g., the 5 volt driver) 201a and the demultiplexer 207 are essentially unchanged.